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RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITING PROCEDURE
EXAMINING GROUP 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lin, et al. Docket No.: TSM03-0670
Serial No.: 10/729,092 Art Unit: 2815
Filed: December 5, 2003 Examiner: Matthew E. Warren
For: Structure and Method of Forming Integrated Circuits Utilizing Strained
Channel Transistors

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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RESPONSE TO OFFICE ACTION

Dear Sir:

The following remarks are offered in response to Examiner's Office Action dated June 2, 2006, a final rejection. Applicants respectfully request reconsideration of claims 1-9, 12, 14, 16-17, 22-23, 25-34, 36-38, 40-42, 45-55 and 78-80.

TSM03-0670

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